Indzawo Optic Connect

Optical Coupler Design and NAND Gate





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Several important parameters must be accurately analyzed during the design of the NAND-LG with the PC-SOA structure based on the MZI, i.e., the biasing current, group index, input ...



The response time for all input combinations ranges from 261.52 ps to 262.87 ps. This design utilizes two Mach-Zehnder interferometers (MZIs) for optimized NAND gate functionality. The text aims to ...



In the present work we propose a novel method for an all-optical logic operation using a simple bi-directional coupler with unequal length of waveguide. The simulated results show that the logic ...



Silicon on Insulator and directional optical coupler based AND, OR, NOR and NAND gates are designed in this paper. The Silicon On Insulator (SOI) waveguide with



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Our work will provide a powerful strategy on large-bandwidth all-optical circuits for high-density data processing in the future.



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In this paper a novel all-optical logic NAND, NOR and XOR gate based on nonlinear directional coupler theory is demonstrated. We use the identical structure which contains three ...

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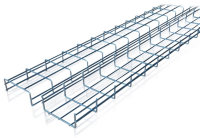
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cascading of diffractive NAND gates is also possible: using all the optical logical values and the output optical waves presented in Fig. 2(a), the diffractive NAND gate



This study explores optical systems as a promising alternative, leveraging the speed of photons over electrons. Specifically, we design and simulate optical NAND and NOR logic gates ...



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Using MIM waveguides, some designs of directional coupler (DC) and non-linear Mach–Zehnder interferometers (MZIs) have been proposed and serving as a house for optical circuit ...

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	In this paper, the ring-resonator-driven logic gate was designed to perform proposed logical operations such as NAND and NOR using a 2D PhC with a rod-type square lattice in a silicon ...
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