

# Cautions for Core Switch Debugging



## Overview

Because debugging output is assigned high priority in the CPU process, it can render the system unusable. For this reason, use the debug commands only to troubleshoot specific problems or during troubleshooting sessions with Cisco technical support staff. Because. Hi, can anyone advise whether it is safe to run debug commands "debug platform software memory <process> <R0/R1/RP xxx > alloc callsite start " and "debug platform software memory. alloc backtrace start " ?

on c9407r switch?

we saw the error log on the switch. Is it software bug?

the XE OS. Is this a good idea to perform a "maintenance" reboot just because?

There's a pretty good chance that on a 6500 these days that a reboot or powercycle of the chassis you'll have at least something die. Both 10 gig ones.

External JTAG equipment can be used to: Download and debug programs. Download new images to the PLD on the Core Tile or to FPGAs or PLDs present on other attached boards. To process the many tasks within an embedded system, processors that contain different core types are increasingly used.

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**WARNING:** I bring this command for completeness sake, but this command will run dozens/hundreds of debug commands, printing lots of info, hundreds of pages, ...



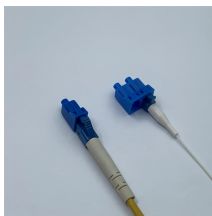
These commands are helpful in diagnosing and resolving internetworking problems and should be enabled only under the guidance of Cisco technical support staff. Because debugging output is ...



My question is: when i run the debug, how come I don't see anything? I don't see any debugs populating my screen. Where do they all go? I know for sure it works because I've seen a co ...



Recently the server guy in my team has been proposing to reboot the 6509 core switches as a matter of practice in order to "clear stuff out". As of last night, core1 was up for two years, four months, and ...



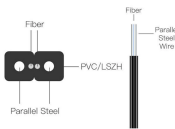
The JTAG connector provides a set of signals that allow debugging equipment to be used. When debugging a development system with multiple tiles, connect the JTAG debugging equipment to the ...



The general process of debugging will be finding a point in the code in which we would like to stop execution and observe the values stored in either ...



In general, debug commands should be run in a lab environment first. However, if you need to run the debug in a production environment, then definitely only during maintenance time and ...



To enable debugging for all features on the switch, use the debug all command in EXEC mode. To disable this command and turn off all debugging, use the no form of the command.



This appendix describes the debug privileged EXEC commands that have been created or changed for use with the Catalyst 2960 switch. These commands are helpful in diagnosing and resolving ...



It is recommended to have the debug logs enabled only during the debugging phase. The logs will hinder the performance and memory consumption of the application.



Enabling a core dump in the event of a crash: There are 3 main ways to do it, depending on one's environment and what is easiest. The easiest way is dumping to flash.



To be able to properly debug such a system, two conditions must be met: The multicore processor must have suitable on-chip debug and trace logic. The development environment must support debugging ...



I'm going to put all the correct port entries in the switches for the APs, I'm also going to make sure the AP's are setup correctly. I also noticed that despite the switches having POE in them, ...



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## Contact Us

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